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L Number	Hits	Search Text	DB	Time stamp
1	324	((supply\$4 or power\$4)near3 computer)with (((power\$3 adj down) or (power\$3 adj off))adj2 (state or mode)), ((supply\$4 or power\$4)near3 computer)with (((power\$3 adj down) or (power\$3 adj off))adj2 (state or mode))) same ((disabl\$4 or suspending) with (I/O or peripheral or input/output))	USPAT; US-PGPUB; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 15:58
2	9	((supply\$4 or power\$4)near3 computer)with (((power\$3 adj down) or (power\$3 adj off))adj2 (state or mode))) same ((disabl\$4 or suspending) with (I/O or peripheral or input/output))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 13:50
3	159	((supply\$4 or power\$4)near3 computer)with (((power\$3 adj down) or (power\$3 adj off))adj2 (state or mode))) same ((I/O or peripheral or input/output)adj device\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 13:50
4	1	((supply\$4 or power\$4)near3 computer)with (((power\$3 adj down) or (power\$3 adj off))adj2 (state or mode))) and (BIOS with ((disabl\$4 or suspending) adj5 (I/O or peripheral or input/output)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 13:51
5	1	((supply\$4 or power\$4)near3 computer)with (((power\$3 adj down) or (power\$3 adj off))adj2 (state or mode))) and ((boot adj code) or BIOS) with ((disabl\$4 or suspending) adj5 (I/O or peripheral or input/output)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 13:52
7	3	((boot adj code) or BIOS) near6 ((disabl\$4 or suspending) near3 (I/O or peripheral or input/output))) and (((power\$3 adj down) or (power\$3 adj off))adj2 (state or mode))	USPAT; US-PGPUB; IBM_TDB	2004/10/27 13:53
6	12	((boot adj code) or BIOS) near6 ((disabl\$4 or suspending) near3 (I/O or peripheral or input/output)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 14:39
8	50	(suspen\$7 or turn\$on or switch\$4 or transfer\$4 or chang\$4 or alter\$6)near3 ((powered adj down)adj (mode or state))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 14:56
9	109	(S3 or C3)adj (pc or computer or notebook or laptop)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 15:00
10	5791	(reduc\$6 or minimiz\$7 or lower\$3 or smaller or quick\$3 or faster or speedy or expeditat\$4)near3 (boot\$4 or (boot\$4 adj time))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 15:04
11	4	(restor\$4)near3 ((powered adj down)adj2 (state or mode))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 15:32
12	25	(restor\$4 or suspend\$4)near3 ((powered adj down)adj2 (state or mode))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 15:55
13	161	acpi near3 control	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 15:55



-	118	(reduc\$6 or decreas\$4 or minimiz\$7 or optimiz\$6 or limit\$7)near5 (boot\$4 adj time)	USPAT; US-PGPUB; IBM_TDB	2004/10/19 17:07
-	1	10/613319 and harrington	USPAT; US-PGPUB; IBM_TDB	2004/10/19 17:07
-	5	suspend near3 (memory adj state)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:20
-	9	(suspend or (power adj down)) near3 (memory adj state)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:38
-	44	(suspend or (power adj down) or ACPI or S3) near3 ((RAM or memory) adj state)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:51
-	7	((suspend or (power adj down) or ACPI or S3) near3 ((RAM or memory) adj state)) and ((quick\$4 or fast\$3 or speedy or sav\$4 or reduc\$5)near4 boot\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:41
-	7	((suspend or (power adj down) or ACPI or S3) near3 ((RAM or memory) adj state)) and ((quick\$4 or fast\$3 or speedy or reduc\$5)near4 boot\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:40
-	4509	((quick\$4 or fast\$3 or speedy or sav\$4 or reduc\$5 or prompt\$4)near4 boot\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:57
-	8	((suspend or (power adj down) or ACPI or S3) near3 ((RAM or memory) adj state)) and ((quick\$4 or fast\$3 or speedy or sav\$4 or reduc\$5 or prompt\$4)near4 boot\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 15:52
-	1157	((quick\$4 or fast\$3 or speedy or sav\$4 or reduc\$5 or prompt\$4)near4 boot\$4) ) and (((suspend or (power adj down) or ACPI or S3) near3 ((RAM or memory) adj state))or (suspend-to-(ram or memory)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:56
-	1157	((quick\$4 or fast\$3 or speedy or sav\$4 or reduc\$5 or prompt\$4)near4 boot\$4) ) and (((suspend or (power adj down) or ACPI or S3) near3 (mode or state))or (suspend-to-(ram or memory)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 14:59
-	3049	((quick\$4 or fast\$3 or speedy or sav\$4 or reduc\$5 or prompt\$4)near4 boot\$4)	USPAT; US-PGPUB; IBM_TDB	2004/10/20 14:57
-	414	((quick\$4 or fast\$3 or speedy or sav\$4 or reduc\$5 or prompt\$4)near4 boot\$4) ) same (((suspend or (power adj down) or ACPI or S3) near3 (mode or state))or (suspend-to-(ram or memory)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 15:04
-	8	((quick\$4 or fast\$3 or speedy or sav\$4 or reduc\$5 or prompt\$4)near4 boot\$4) ) same (((suspend or (power adj down) or ACPI or S3) near3 (mode or state)))	USPAT; US-PGPUB; IBM_TDB	2004/10/20 15:52
-	95	((quick\$4 or fast\$3 or speedy or sav\$4 or reduc\$5 or prompt\$4)near4 boot\$4) ) and (((suspend or (power adj down) or ACPI or S3) near3 (mode or state)))	USPAT; US-PGPUB; IBM_TDB	2004/10/20 15:04

	7	((quick\$4 or fast\$3 or speedy or sav\$4 or reduc\$5 or prompt\$4)near4 boot\$4) ) and (s3 adj (state or mode))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 15:05
-	21227	713/\$.cccls.	USPAT; US-PGPUB; IBM_TDB	2004/10/20 15:52
-	342	713/\$.cccls. and ((quick\$4 or fast\$3 or speedy or sav\$4 or reduc\$5 or prompt\$4 or lower\$4)near4 boot\$4)	USPAT; US-PGPUB; IBM_TDB	2004/10/20 15:54
-	24	(713/\$.cccls. and ((quick\$4 or fast\$3 or speedy or sav\$4 or reduc\$5 or prompt\$4 or lower\$4)near4 boot\$4) ) and (plac\$4 near6 (suspend adj4 (state or mode)))	USPAT; US-PGPUB; IBM_TDB	2004/10/20 15:59
-	9	(713/\$.cccls. and ((quick\$4 or fast\$3 or speedy or sav\$4 or reduc\$5 or prompt\$4 or lower\$4)near4 boot\$4) ) and (s3 adj4 (state or mode))	USPAT; US-PGPUB; IBM_TDB	2004/10/20 16:01
-	551	(plac\$3 or enabl\$4 or activat\$4 or put\$4)near6 ((s3 or suspend)adj4 (state or mode))	USPAT; US-PGPUB; IBM_TDB	2004/10/20 16:14
-	14	((plac\$3 or enabl\$4 or activat\$4 or put\$4)near6 ((s3 or suspend)adj4 (state or mode)) and (disabl\$4 near3 devices)	USPAT; US-PGPUB; IBM_TDB	2004/10/20 16:04
-	1280	(switch\$4 or transfer\$4 or mov\$4 or chang\$4)near6 ((s3 or suspend)adj4 (state or mode))	USPAT; US-PGPUB; IBM_TDB	2004/10/20 16:16
-	751	(switch\$4 or transfer\$4 or mov\$4 or chang\$4)adj4 ((s3 or suspend)adj4 (state or mode))	USPAT; US-PGPUB; IBM_TDB	2004/10/20 16:17
-	46	((switch\$4 or transfer\$4 or mov\$4 or chang\$4)near6 ((s3 or suspend)adj4 (state or mode)) and ACPI	USPAT; US-PGPUB; IBM_TDB	2004/10/20 16:17
-	3	((switch\$4 or transfer\$4 or mov\$4 or chang\$4)near6 ((s3 or suspend)adj4 (state or mode)) and ((quick\$4 or fast\$4 or speed\$3 or sav\$4 or reduc\$4)near3 (boot\$4 near2 time))	USPAT; US-PGPUB; IBM_TDB	2004/10/20 16:19
-	359	(s3 adj3 (pc or computer or notebook or laptop))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/21 17:10
-	0	((s3 adj3 (pc or computer or notebook or laptop)) same ((disabl\$4 or terminat\$4)near3 (input\$output or i/O))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/21 17:12
-	3	((s3 near3 (pc or computer or notebook or laptop)) same ((disabl\$4 or terminat\$4)near3 (input\$output or i/O))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/21 17:16
-	4	((s3 near3 (pc or computer or notebook or laptop)) and ((disabl\$4 or terminat\$4)near3 (input\$output or i/O))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/21 17:14
-	643	(s3 near3 (pc or computer or notebook or laptop))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/21 17:16
-	16	((s3 near3 (pc or computer or notebook or laptop)) same (acpi)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/21 17:19





-		7	((switch\$4 or chang\$4 or alter\$6)adj4 ((suspend adj3 (ram or memory))or (low adj power)or str or s3)) and (( Boot or power\$on or active or on)adj3 time))) and (((switch\$4 or chang\$4 or alter\$6)adj4 ((suspend adj3 (ram or memory))or (low adj power)or str or s3)) and ((expedi\$7 or accelerat\$4 or fast\$3 or instant\$6 or quick\$3)near3 (restor\$4 or initiali\$7 or boot\$5 or normal))) 6438708.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 07:21
-		2	5933590.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT;	2004/10/27 10:05
-		2	5941992.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT;	2004/10/27 07:32
-		2	5958063.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT;	2004/10/27 07:33
-		2	5999103.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT;	2004/10/27 07:33
-		2	6130876.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT;	2004/10/27 07:34
-		2	4686386.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT;	2004/10/27 07:34
-		4	4137563.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT;	2004/10/27 07:34
-		2	4698748.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT;	2004/10/27 07:35
-		2	4893271.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT;	2004/10/27 07:37
-		2	09188246.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT;	2004/10/27 07:37
-	22782		(detect\$4 ot determin\$4 or deci\$6)near6 ((Power adj down) or S3 or sleep or suspend)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:12

-		552	((detect\$4 ot determin\$4 or deci\$6)near6 ((Power adj down) or S3 or sleep or suspend)) same (resum\$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:08
-		1	((detect\$4 ot determin\$4 or deci\$6)near6 ((Power adj down) or S3 or sleep or suspend)) same (((disabl\$4 or suspending)near3 power)with (I/O or peripheral or input/output))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:12
-		19	((detect\$4 ot determin\$4 or deci\$6)near6 ((Power adj down) or S3 or sleep or suspend)) and (((disabl\$4 or suspending)near3 power)with (I/O or peripheral or input/output))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:13
-		2491	(detect\$4 ot determin\$4 or deci\$6)near6 (((Power adj down) or S3 or sleep or suspend)adj3 (state or mode))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:14
-		3	((detect\$4 ot determin\$4 or deci\$6)near6 (((Power adj down) or S3 or sleep or suspend)adj3 (state or mode))) and (((disabl\$4 or suspending)near3 power)with (I/O or peripheral or input/output))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:16
-		5075	(resum\$6 or start\$4 or switch\$4 or chang\$4 or transition\$4 or alter\$6)near6 (((Power adj down) or S3 or sleep or suspend)adj3 (state or mode))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:15
-		3737	(resum\$6 or start\$4 or switch\$4 or chang\$4 or transition\$4 or alter\$6)adj6 (((Power adj down) or S3 or sleep or suspend)adj3 (state or mode))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:29
-		0	((resum\$6 or start\$4 or switch\$4 or chang\$4 or transition\$4 or alter\$6)adj6 (((Power adj down) or S3 or sleep or suspend)adj3 (state or mode))) same (((disabl\$4 or suspending)near3 power)with (I/O or peripheral or input/output))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:16
-		2	((resum\$6 or start\$4 or switch\$4 or chang\$4 or transition\$4 or alter\$6)adj6 (((Power adj down) or S3 or sleep or suspend)adj3 (state or mode))) same (((disabl\$4 or suspending)with (I/O or peripheral or input/output) or irrelevan\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:17
-		2	((resum\$6 or start\$4 or switch\$4 or chang\$4 or transition\$4 or alter\$6)adj6 (((Power adj down) or S3 or sleep or suspend)adj3 (state or mode))) same (((disabl\$4 or suspending)with (I/O or peripheral or input/output))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:17
-		0	((resum\$6 or start\$4 or switch\$4 or chang\$4 or transition\$4 or alter\$6)adj6 (((Power adj down) or S3 or sleep or suspend)adj3 (state or mode))) same (((disabl\$4 or suspending))near6 (I/O or peripheral or input/output))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:30
-		93	((resum\$6 or start\$4 or switch\$4 or chang\$4 or transition\$4 or alter\$6)adj6 (((Power adj down) or S3 or sleep or suspend)adj3 (state or mode))) and (((disabl\$4 or suspending))near6 (I/O or peripheral or input/output))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:18

	419	((resum\$6 or start\$4 or switch\$4 or chang\$4 or transition\$4 or alter\$6)adj6 (((Power adj down) or S3 or sleep or suspend)adj3 (state or mode)) and (boot\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:29
	23	((resum\$6 or start\$4 or switch\$4 or chang\$4 or transition\$4 or alter\$6)adj6 (((Power adj down) or S3 or sleep or suspend)adj3 (state or mode)) and (((disabl\$4 or suspending)near6 (I/O or peripheral or input/output)) and (boot\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:18
	53	((resum\$6 or start\$4 or switch\$4 or chang\$4 or transition\$4 or alter\$6)adj6 (((Power adj down) or S3 or sleep or suspend)adj3 (state or mode)) same (boot\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:21
	473	(resum\$6 or start\$4 or switch\$4 or chang\$4 or transition\$4 or alter\$6)adj6 (((Power adj down))adj3 (state or mode))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:29
	0	((resum\$6 or start\$4 or switch\$4 or chang\$4 or transition\$4 or alter\$6)adj6 (((Power adj down))adj3 (state or mode)) same (((disabl\$4 or suspending)near6 (I/O or peripheral or input/output)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:31
	0	((resum\$6 or start\$4 or switch\$4 or chang\$4 or transition\$4 or alter\$6)adj6 (((Power adj down))adj3 (state or mode)) same (((disabl\$4 or suspending or terminat\$4)near6 (I/O or peripheral or input/output)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:31
	9	((resum\$6 or start\$4 or switch\$4 or chang\$4 or transition\$4 or alter\$6)adj6 (((Power adj down))adj3 (state or mode)) and (((disabl\$4 or suspending)near6 (I/O or peripheral or input/output)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:56
	1782	(s3 or c3)adj3 (processor or controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:57
	1338	(s3 or c3)adj2 (processor or controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:57
	730	(s3 or c3)adj (processor or controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 10:57
	700	(s3 or c3)adj (processor or controller)	USPAT; US-PGPUB; IBM_TDB	2004/10/27 10:57
	0	((s3 or c3)adj (processor or controller)) and ((rapid or fast\$4 or quick\$4) adj3 boot\$4)	USPAT; US-PGPUB; IBM_TDB	2004/10/27 10:58
	0	((s3 or c3)adj (processor or controller)) and ((reduc\$6 or shorten\$4 or minimiz\$7) adj3 (boot\$4 adj time))	USPAT; US-PGPUB; IBM_TDB	2004/10/27 13:37

L Number	Hits	Search Text	DB	Time stamp
1	324	((supply\$4 or power\$4)near3 computer)with (((power\$3 adj down) or (power\$3 adj off))adj2 (state or mode))	USPAT; US-PGPUB; IBM_TDB	2004/10/27 16:51
2	9	((supply\$4 or power\$4)near3 computer)with (((power\$3 adj down) or (power\$3 adj off))adj2 (state or mode)) same ((disabl\$4 or suspending) with (I/O or peripheral or input/output))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 13:50
3	159	((supply\$4 or power\$4)near3 computer)with (((power\$3 adj down) or (power\$3 adj off))adj2 (state or mode)) same ((I/O or peripheral or input/output)nadj device\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 13:50
4	1	((supply\$4 or power\$4)near3 computer)with (((power\$3 adj down) or (power\$3 adj off))adj2 (state or mode)) and (BIOS with ((disabl\$4 or suspending) adj5 (I/O or peripheral or input/output)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 13:51
5	1	((supply\$4 or power\$4)near3 computer)with (((power\$3 adj down) or (power\$3 adj off))adj2 (state or mode)) and ((boot adj code) or BIOS) with ((disabl\$4 or suspending) adj5 (I/O or peripheral or input/output)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 13:52
7	3	((boot adj code) or BIOS) near6 ((disabl\$4 or suspending) near3 (I/O or peripheral or input/output))) and (((power\$3 adj down) or (power\$3 adj off))adj2 (state or mode))	USPAT; US-PGPUB; IBM_TDB	2004/10/27 13:53
6	12	((boot adj code) or BIOS) near6 ((disabl\$4 or suspending) near3 (I/O or peripheral or input/output)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 14:39
8	50	(suspen\$7 or turn\$on or switch\$4 or transfer\$4 or chang\$4 or alter\$6)near3 ((powered adj down)adj (mode or state))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 16:40
9	109	(S3 or C3)adj (pc or computer or notebook or laptop)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 15:00
10	5791	(reduc\$6 or minimiz\$7 or lower\$3 or smaller or quick\$3 or faster or speedy or expeditat\$4)near3 (boot\$4 or (boot\$4 adj time))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 15:04
11	4	(restor\$4)near3 ((powered adj down)adj2 (state or mode))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 15:32
12	25	(restor\$4 or suspend\$4)near3 ((powered adj down)adj2 (state or mode))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 15:55
13	161	acpi near3 control	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 15:55

14	58	acpi adj control	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 15:55
15	101	acpi adj3 control	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 15:56
16	17	(acpi adj3 control) and (boot\$4 adj time)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 15:56
17	115	wak\$5 near4 (((power\$3 adj down) or (power\$3 adj off))adj2 (state or mode))	USPAT; US-PGPUB; IBM_TDB	2004/10/27 16:39
18	1	(wak\$5 near4 (((power\$3 adj down) or (power\$3 adj off))adj2 (state or mode))) and ((S3 or C3 or (suspend adj3 (ram or memory)))adj (state or mode))	USPAT; US-PGPUB; IBM_TDB	2004/10/27 16:37
19	21289	713/.cccls.	USPAT; US-PGPUB; IBM_TDB	2004/10/27 16:34
20	4673	719/.cccls.	USPAT; US-PGPUB; IBM_TDB	2004/10/27 16:36
21	2	719/.cccls. and (((supply\$4 or power\$4)near3 computer)with (((power\$3 adj down) or (power\$3 adj off))adj2 (state or mode)))	USPAT; US-PGPUB; IBM_TDB	2004/10/27 16:36
22	7258	717/.cccls.	USPAT; US-PGPUB; IBM_TDB	2004/10/27 16:36
23	3	717/.cccls. and (((supply\$4 or power\$4)near3 computer)with (((power\$3 adj down) or (power\$3 adj off))adj2 (state or mode)))	USPAT; US-PGPUB; IBM_TDB	2004/10/27 16:36
25	4	717/.cccls. and ((S3 or C3 or (suspend adj3 (ram or memory)))adj (state or mode))	USPAT; US-PGPUB; IBM_TDB	2004/10/27 16:37
24	8	719/.cccls. and ((S3 or C3 or (suspend adj3 (ram or memory)))adj (state or mode))	USPAT; US-PGPUB; IBM_TDB	2004/10/27 16:38
26	0	717/.cccls. and (wak\$5 near4 (((power\$3 adj down) or (power\$3 adj off))adj2 (state or mode)))	USPAT; US-PGPUB; IBM_TDB	2004/10/27 16:39
27	0	719/.cccls. and (wak\$5 near4 (((power\$3 adj down) or (power\$3 adj off).)adj2 (state or mode)))	USPAT; US-PGPUB; IBM_TDB	2004/10/27 16:39
28	0	719/.cccls. and ((suspen\$7 or turn\$on or switch\$4 or transfer\$4 or chang\$4 or alter\$6)near3 ((powered adj down)adj (mode or state)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 16:41
29	1	717/.cccls. and ((suspen\$7 or turn\$on or switch\$4 or transfer\$4 or chang\$4 or alter\$6)near3 ((powered adj down)adj (mode or state)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/27 16:41
30	21289	713/.cccls.	USPAT; US-PGPUB; IBM_TDB	2004/10/27 16:47
31	390	713/.cccls. and (boot\$ adj time)	USPAT; US-PGPUB; IBM_TDB	2004/10/27 16:58
32	27	713/.cccls. and ((reduced or minimized or short\$4 or quick\$4 or fast\$4)near4 (boot\$ adj time))	USPAT; US-PGPUB; IBM_TDB	2004/10/27 16:50

33	770	713/.cccls. and (((power\$3 adj down) or (power\$3 adj off))adj2 (state or mode))	USPAT; US-PGPUB; IBM_TDB	2004/10/27 16:51
34	27	(713/.cccls. and (boot\$ adj time)) and (((power\$3 adj down) or (power\$3 adj off))adj2 (state or mode))	USPAT; US-PGPUB; IBM_TDB	2004/10/27 16:51
35	5	713/.cccls. and (sav\$4 near3 (boot\$ adj time))	USPAT; US-PGPUB; IBM_TDB	2004/10/27 17:01
36	8	713/.cccls. and (sav\$4 near3 ((initiali\$6 or boot\$4 or configur\$7) adj time))	USPAT; US-PGPUB; IBM_TDB	2004/10/27 17:01

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## 1 Compiler optimizations for power, performance: Compiler parallelization of C programs for multi-core DSPs with multiple address spaces

Björn Franke, M.F.P. O'Boyle

October 2003 **Proceedings of the 1st IEEE/ACM/IFIP international conference on Hardware/software codesign and system synthesis**

Full text available:  pdf(407.08 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper develops a new approach to compiling C programs for multiple address space, multi-processor DSPs. It integrates a novel data transformation technique that exposes the processor location of partitioned data into a parallelization strategy. When this is combined with a new address resolution mechanism, it generates efficient programs that run on multiple address spaces without using message passing. This approach is applied to the UTDSP benchmark suite and evaluated on a four processor ...

**Keywords:** DSPs, address resolution, data partitioning, multiple address space compilation

## 2 VLSI in the nanometer era: Modeling QCA for area minimization in logic synthesis

Nadine Gergel, Shana Craft, John Lach

April 2003 **Proceedings of the 13th ACM Great Lakes symposium on VLSI**

Full text available:  pdf(176.72 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Concerned by the wall that Moore's Law is expected to hit in the next decade, the integrated circuit community is turning to emerging nanotechnologies for continued device improvements. While significant advancements in nanotechnology devices have been achieved, much work is required to integrate these technologies into the existing design methodologies. Given that the physical design paradigm of each nanotechnology will be significantly different than that of traditional silicon circuits, the u ...

**Keywords:** CAD, QCA, interconnect, logic synthesis, nanotechnology

## 3 VLSI in the nanometer era: CMOS flash analog-to-digital converter for high speed and low voltage applications

Jincheol Yoo, Kyusun Choi, Jahan Ghaznavi

April 2003 **Proceedings of the 13th ACM Great Lakes symposium on VLSI**

Full text available:  pdf(523.22 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

A CMOS flash analog-to-digital converter (ADC) designed for high speed and low voltage is presented. Using the Threshold Inverter Quantization (TIQ) comparator technique, a flash ADC can be applied to low supply voltage. A fat tree encoder that has signal delay of  $O(\log_2 N)$  is used for performance. A 6-bit and an 8-bit flash ADC were designed with  $0.07 \mu\text{m}$  CMOS technology and 0.7 V power supply voltage. The 6-bit ADC operates up to 4.76 giga samples per ...

**Keywords:** TIQ comparator, analog-to-digital converter, fat tree encoder, flash ADC, high speed, low voltage

#### **4 VLSI in the nanometer era: Exploiting multiple functionality for nano-scale reconfigurable systems**

Paul Beckett

April 2003 **Proceedings of the 13th ACM Great Lakes symposium on VLSI**

Full text available:  pdf(197.22 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

It is likely that it will become increasingly difficult to manufacture the complex, heterogeneous logic structures that characterise current reconfigurable logic systems. As a result, these systems may come to be characterised by vast arrays of largely identical devices that are differentiated via post-fabrication configuration - but only if low-overhead configuration can be achieved. Two simulation studies are presented that describe some ideas for achieving low-overhead reconfigurability in sy ...

**Keywords:** RTD, carbon nanotube, chalcogenide, double gate transistors, multi-valued RAM, multiple functionality, nanoelectronics, nanotechnology, reconfigurable systems, resonant tunneling

#### **5 Links: Evidence of Hypertext in the scholarly archive**

Tim Brody, Leslie Carr, Stevan Harnad

June 2002 **Proceedings of the thirteenth ACM conference on Hypertext and hypermedia**

Full text available:  pdf(163.58 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

This paper attempts to substantiate recent observations about the development of hypertext rhetoric in scholarly archives by reporting the results of some simple quantitative studies of the use by researchers of a major scholarly archive.

**Keywords:** hypertext rhetoric, navigation, scholarly and scientific communication, textuality, web

#### **6 Workshops: Physiological computing**

Jennifer Allanson, Gillian M. Wilson

April 2002 **CHI '02 extended abstracts on Human factors in computing systems**

Full text available:  pdf(133.03 KB) Additional Information: [full citation](#), [abstract](#), [citations](#)

Applications involving the measurement of human physiological responses to environment are becoming increasingly popular in HCI. This is due in part to the increasing availability of low-cost, high-specification sensing technologies. Areas such as HCI evaluation, affective computing and biofeedback-based brain-computer interaction are all benefiting from the rich data source physiological sensing technologies make available. However, guidelines on the gathering and analysis of these measurements ar ...

**Keywords:** HCI evaluation, affective computing, biofeedback, brain-computer interaction, physiological computing

**7 S3, the System and Software Simulator**

Leo J. Cohen

December 1968 **Proceedings of the second conference on Applications of simulations**

Full text available:  pdf(250.73 KB) Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

The System and Software Simulator (S3) is a computer program written entirely in Fortran IV and capable of execution on any computer having that compiler available. Thus far S3 has been applied to computer system simulation problems while executing on the Univac 1108 and the IBM system 360, models 50, 65, and 75. The purpose of S3 is threefold; in the first place it provides a convenient computer oriented language for the specification of the total hardware/software environment o ...

**8 S3: similarity search in CAD database systems**

Stefan Berchtold, Hans-Peter Kriegel

June 1997 **ACM SIGMOD Record , Proceedings of the 1997 ACM SIGMOD international conference on Management of data**, Volume 26 Issue 2

Full text available:  pdf(587.52 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

S3 is the prototype of a database system supporting the management and similarity retrieval of industrial CAD parts. The major goal of the system is to reduce the cost for developing and producing new parts by maximizing the reuse of existing parts. S3 supports the following three types of similarity queries: query by example (of an existing part in the database), query by sketch and thematic similarity query. S3 is an object-oriented system offering an adequate graphical user interface. On ...

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C. Heitmeyer, C. Landwehr, M. Cornwell

April 1982 **ACM SIGSOFT Software Engineering Notes , Proceedings of the workshop on Rapid prototyping**, Volume 7 Issue 5Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

This paper describes the role of quick prototyping in the Secure Military Message Systems project. The issues that are being explored with quick prototyping are identified. To date, a small prototype message system written in Franz Lisp has been implemented on a Vax 11/780. The guidelines observed and the lessons learned in constructing the prototype are discussed.

**42 Quick calculation of Jacobian elliptic functions**

Herbert E. Salzer

July 1962 **Communications of the ACM**, Volume 5 Issue 7Full text available:  [pdf\(87.32 KB\)](#) Additional Information: [full citation](#), [references](#)**43 Should democracy online be quick, strong, or thin?**

Joachim Åström

January 2001 **Communications of the ACM**, Volume 44 Issue 1Full text available:  [pdf\(72.41 KB\)](#)  [html\(14.55 KB\)](#) Additional Information: [full citation](#), [references](#), [index terms](#)**44 A quick check of network performance**

Jeffrey T. Hicks, John Q. Walker

January 2001 **International Journal of Network Management**, Volume 11 Issue 1Full text available:  [pdf\(372.57 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Have you ever tried to measure the response time across a network? Do you sometimes wonder what throughput rate you're getting over a particular link? Are you concerned about the impact of adding streaming multimedia traffic to a network? Would you like to know the exact route your data is taking? Individual tools are available to measure the throughput and response time of your applications, trace a network route, or test a network's capacity for handling ...

**45 Building a Firewall with IP Chains: A quick introduction to the program ipchains**

Pedro Bueno

December 1999 **Linux Journal**Full text available:  [html\(5.71 KB\)](#)Additional Information: [full citation](#), [references](#), [index terms](#)**46 The Quick Road to an Intranet Web Server: Apache and Linux make the task simple**

Russell C. Pavlicek

November 1998 **Linux Journal**Full text available:  [html\(14.95 KB\)](#)Additional Information: [full citation](#), [references](#), [index terms](#)**47 The Quick Start Guide to the GIMP, Part Four**

Michael J. Hammel

February 1998 **Linux Journal**Full text available:  [html\(34.35 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Our series winds up with a detailed description of the toolbox, plug-ins and keyboard acceleration

**48 The Quick Start Guide to the GIMP, Part 3**

Michael J. Hammel

January 1998 **Linux Journal**Full text available:  [html\(28.96 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This month we learn how to use the Image Window and layers in building our images with the GIMP, a Linux power tool for the graphics artist

**49 The Quick Start Guide to the GIMP, Part 2**

Michael J. Hammel

December 1997 **Linux Journal**Full text available:  [html\(18.62 KB\)](#)Additional Information: [full citation](#), [references](#), [index terms](#)**50 The Quick Start Guide to the GIMP, Part 1**

Michael J. Hammel

November 1997 **Linux Journal**Full text available:  [html\(17.38 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

First of a four-part series introducing the GIMP, a Linux power tool for the graphics artist

**51 Methods & tools: Playacting and focus troupes:: theater techniques for creating quick, intense, immersive, and engaging focus group sessions**

Steve Sato, Tony Salvador

September 1999 **Interactions**, Volume 6 Issue 5Full text available:  [pdf\(171.35 KB\)](#)Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#) [html\(25.00 KB\)](#)

**52** Quick GOMS: a visual software engineering tool for simple rapid time-motion modeling 

David V. Beard, Scott Entrikin, Pat Conroy, Nathan C. Wingert, Corey D. Schou, Dana K. Smith, Kevin M. Denelsbeck  
May 1997 **interactions**, Volume 4 Issue 3

Full text available:  [pdf\(1.23 MB\)](#) Additional Information: [full citation](#), [references](#), [index terms](#)

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**21 Special issue on distributed computing: Quick-tests for characterizing distributed systems**

Rick Cormier, Ed Guy, David E Ruddock

April 1999 **ACM SIGAPP Applied Computing Review**, Volume 7 Issue 1Full text available:  pdf(396.61 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper discusses some problems caused by inadequately characterizing distributed and system architectures. It presents a classification system for decomposing distributed systems and presents a few simple test cases. To allow system designers to characterize different aspects of their environments, we have started a collection of simple Quick-Tests.

**22 Book Reviews: The Web Publisher's Illustrated Quick Reference, Ralph Grabowski**

W. M. Waite

April 1997 **ACM SIGOPS Operating Systems Review**, Volume 31 Issue 2Full text available:  pdf(33.96 KB) Additional Information: [full citation](#)

**23 Quick piping: a fast, high-level model for describing processor pipelines**

Christopher W. Milner, Jack W. Davidson

June 2002 **ACM SIGPLAN Notices , Proceedings of the joint conference on Languages, compilers and tools for embedded systems: software and compilers for embedded systems**, Volume 37 Issue 7Full text available:  pdf(206.06 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Responding to marketplace needs, today's embedded processors must feature a flexible core that allows easy modification with fast time to market. In this environment, embedded processors are increasingly reliant on flexible support tools. This paper presents one such tool, called *Quick Piping*, a new, high-level formalism for modeling processor pipelines. *Quick Piping* consists of three primary components that together provide an easy-to-build, reusable processor description:

**Keywords:** embedded systems, modeling of computer architecture, pipelines

**24 Software support: preempting the quick question**

Lauri Loebel

December 1987 **Proceedings of the 15th annual ACM SIGUCCS conference on User Services**

Full text available:  pdf(308.15 KB) Additional Information: [full citation](#), [index terms](#)



**25 "I've got a quick question..." or, a dozen years of network user services**

Christine Wendt

December 1987 **Proceedings of the 15th annual ACM SIGUCCS conference on User Services**

Full text available:  pdf(103.45 KB) Additional Information: [full citation](#), [abstract](#), [index terms](#)

The lead-in never changes, but the "quick" questions do. In 12 years at the Merit Computer Network, the quick questions have tended to come in waves whose nature has changed dramatically as the nature of network usage and network services has changed.



**26 Fast-Start: quick fault recovery in oracle**

Tirthankar Lahiri, Amit Ganesh, Ron Weiss, Ashok Joshi

May 2001 **ACM SIGMOD Record , Proceedings of the 2001 ACM SIGMOD international conference on Management of data**, Volume 30 Issue 2

Full text available:  pdf(78.85 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Availability requirements for database systems are more stringent than ever before with the widespread use of databases as the foundation for ebusiness. This paper highlights *Fast-Start™ Fault Recovery*, an important availability feature in Oracle, designed to expedite recovery from unplanned outages. Fast-Start allows the administrator to configure a running system to impose predictable bounds on the time required for crash recovery. For instance, fast-start allows fine-gr ...



**27 A quick safari through the reconfiguration jungle**

Patrick Schaumont, Ingrid Verbauwheide, Kurt Keutzer, Majid Sarrafzadeh  
June 2001 **Proceedings of the 38th conference on Design automation**

Full text available:  pdf(268.82 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Cost effective systems use specialization to optimize factors such as power consumption, processing throughput, flexibility or combinations thereof. Reconfigurable systems obtain this specialization at run-time. System reconfigurable has a vertical, a horizontal and a time dimension. We organize this design space as the reconfiguration hierarchy, and discuss the design methods that deal with it. Finally, we survey existing commercial platforms that support reconfiguration and situate them i ...



**28 Usability testing: a quick, cheap, and effective method**

Jerilyn Prescott, Matt Crichton

November 1999 **Proceedings of the 27th annual ACM SIGUCCS conference on User services: Mile high expectations**

Full text available:  pdf(198.62 KB) Additional Information: [full citation](#), [references](#), [index terms](#)

**Keywords:** Web pages, readability, usability testing, user-centered design



**29 Tutorial: a quick introduction to software reliability modeling**

Jarrett Rosenberg

May 1999 **Proceedings of the 21st international conference on Software engineering**

Full text available:  pdf(103.39 KB) Additional Information: [full citation](#), [index terms](#)

**Keywords:** models, software reliability, statistics, testing

**30 Do a quick conversion: put all our documentation on the web** 

Ann Amsler

September 1998 **ACM SIGUCCS Newsletter**, Volume 28 Issue 1-3

Additional Information: [full citation](#), [index terms](#)

**31 Hyper-3D paintings in quick time VR: wunderkammer and hyperaesthesia** 

Philip Sanders

July 1999 **ACM SIGGRAPH 99 Conference abstracts and applications**

Additional Information: [full citation](#), [index terms](#)

**32 Designing a WinHelp project for quick conversion to lowest-common-denominator** 

HTML-based help: a case study

Laurie Kantner, Larry Rusinsky

September 1998 **Proceedings of the 16th annual international conference on Computer documentation**

Full text available:  pdf(730.75 KB) Additional Information: [full citation](#), [index terms](#)

**Keywords:** HTML help, WinHelp, conversion, word macros

**33 Do a quick conversion: put all our documentation on the web** 

Ann Amsler

November 1997 **Proceedings of the 25th annual ACM SIGUCCS conference on User services: are you ready?**

Full text available:  pdf(467.70 KB) Additional Information: [full citation](#), [references](#), [index terms](#)

**34 Quick but not so dirty web design: applying empirical conceptual clustering techniques** 

to organise hypertext content

Charles M. Hymes, Gary M. Olson

August 1997 **Proceedings of the conference on Designing interactive systems: processes, practices, methods, and techniques**

Full text available:  pdf(378.81 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

**Keywords:** WWW design, conceptual structure, fast, hypertext structure

**35 Quick conservative causality analysis** 

Ellen M. Sentovich

September 1997 **Proceedings of the 10th international symposium on System synthesis**

Full text available:  pdf(965.90 KB) Additional Information: [full citation](#), [abstract](#), [references](#)  
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The causality problem is that of determining if a combinational circuit with cycles has acceptable behavior: that for all inputs the outputs are well defined and stable. While the problem manifests itself at the circuit level, it usually originates at the system level. It may arise when a system is designed as a collection of modules: when composed, cycles are discovered in the ensemble. One must analyze these cycles to correct possible errors or to capture the correct behavior appropriately for ...

**Keywords:** causality problem, combinational circuit, combinational circuits, conservative algorithm, quick conservative causality analysis, standard logic synthesis techniques

**36** [Optimal importance sampling for quick simulation of highly reliable Markovian systems](#) 

Stephen G. Strickland

December 1993 **Proceedings of the 25th conference on Winter simulation**

Full text available:  pdf(576.83 KB) Additional Information: [full citation](#), [references](#), [citations](#)

**37** [Discrete event simulation for quick service restaurant traffic analysis](#) 

Steven L. Jaynes, John O. Hoffman

December 1994 **Proceedings of the 26th conference on Winter simulation**

Full text available:  pdf(525.20 KB) Additional Information: [full citation](#), [index terms](#)

**38** [Quick and incomplete responses: the semantic approach](#) 

Chung-Dak Shum

December 1993 **Proceedings of the second international conference on Information and knowledge management**

Full text available:  pdf(1.00 MB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

**39** [The World-Wide Web: a quick tour](#) 

David F. DelGreco

September 1993 **ACM SIGWEB Newsletter**, Volume 2 Issue 2

Additional Information: [full citation](#), [index terms](#)

**40** [A quick introduction to the programming language MIKE](#) 

K. Coolsaet

June 1992 **ACM SIGPLAN Notices**, Volume 27 Issue 6

Full text available:  pdf(586.03 KB) Additional Information: [full citation](#), [abstract](#), [index terms](#)

MIKE is a new programming language developed by the author as a base language for the development of algebraic and symbolic algorithms. It is a structured programming language with a MODULA-2-like syntax supporting special features such as transparent dynamic memory management, discriminated union types, operator overloading, data abstraction and parametrized types. This text gives an overview of the main features of the language as of version 2.0.

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Result page: **1** [2](#) [3](#) [next](#)Relevance scale **1 Papers: A portable & quick Japanese parser: QJP**

Masayuki Kameda

August 1996 **Proceedings of the 16th conference on Computational linguistics - Volume 2**Full text available:  [pdf\(607.98 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

QJP is a portable and quick software module for Japanese processing. QJP analyzes a Japanese sentence into segmented morphemes/words with tags and a syntactic *bunsetsu-kakari-uke* structure based on the two strategies, a) Morphological analysis based on character-types and functional-words and b) Syntactic analysis by simple treatment of structural ambiguities and ignoring semantic information. QJP is small, fast and robust, because 1) dictionary size (less than 100KB) and required memory ...

**2 Universal grammar and lexis for quick ramp-up of MT systems**

Sergei Nirenburg, Victor Raskin

August 1998

Full text available:  [pdf\(469.94 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#) [Publisher Site](#)

This paper introduces Boas, a semi-automatic knowledge elicitation system that guides a team of two people through the process of developing the static knowledge sources for a moderate-quality, broad-coverage MT system from any "low-density" language into English in about six months. The paper focuses on some issues in the elicitation of descriptive knowledge in Boas and also the issue of the principled reuse of pre-existing resources, such as a lexicon, an ontology, and an English generation mo ...

**3 Poster Session: Creating usable simplicity in a text-based "quick help" website**

Brian Sierkowski, Carol Rhodes, William Goveia

October 2001 **Proceedings of the 29th annual ACM SIGUCCS conference on User services**Full text available:  [pdf\(116.28 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

In autumn 1999, the Indiana University School of Education Technology Services spawned a plan to change its Web site from a promotional site to a help site. The intent was to offer technology assistance and information as well as provide ETS with a centralized knowledge base. Since ETS is responsible for network services, distance education, computer workstations, and telephone services, the task was onerous. We began the endeavor hoping to decrease users' reliance on our help desk call-in center ...

**Keywords:** information technology, redesign, usability, web site

4 Quick response replenishment: a case study

Russell E. King, Kara Moon

December 1999 **Proceedings of the 31st conference on Winter simulation: Simulation---a bridge to the future - Volume 2**

Full text available:  pdf(419.06 KB) Additional Information: [full citation](#), [references](#), [index terms](#)



5 Quick simulation of ATM buffers with on-off multiclass Markov fluid sources

G. Kesidis, J. Walrand

July 1993 **ACM Transactions on Modeling and Computer Simulation (TOMACS)**, Volume 3 Issue 3

Full text available:  pdf(407.60 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)



**Keywords:** large deviations, queues, rare events, variance reduction

6 ANSI Standard Ada: quick-reference sheet

David A. Smith

July 1984 **ACM SIGAda Ada Letters**, Volume IV Issue 1

Full text available:  pdf(459.40 KB) Additional Information: [full citation](#), [references](#)



7 Planning Your Special Events: A Quick How-to Primer

Christine Oster

November 1994 **ACM SIGGRAPH Computer Graphics**, Volume 28 Issue 4

Full text available:  pdf(149.32 KB) Additional Information: [full citation](#)



8 Quick: a user-interface design kit for non-programmers

Sarah Douglas, Eckehard Doerry, David Novick

August 1990 **Proceedings of the 3rd annual ACM SIGGRAPH symposium on User interface software and technology**

Full text available:  pdf(1.17 MB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)



9 Cartoon Network's "Quick Draw El Kabong"

George Evelyn, Liz Gazzano

August 2001 **ACM SIGGRAPH 2001 video review on Animation theater program**

Full text available:  mov(1:30 MIN)

 mp4(5.39 MB) Additional Information: [full citation](#), [abstract](#)

 mpg(20.42 MB)

Cartoon character "Quick Draw McGraw" becomes EL KABONG the HERO and fights EL BAD GUY with his steel guitar. El Kabong & Babalooie ride into a garishly colored small town in Mexico that is inhabited by Day of the Dead skeleton townfolk. El Bad Guy and his cohorts



are generally out to get the townfolk. El Kabong saves them and heroine Linda Neigh from certain disaster at the end of the piece, and El Bad Guy gets his just desserts.Hand-painted characters and backgrounds were scanned into the MA ...

**Keywords:** Animation Screening Room 2001

**10 Quick and easy cache performance analysis**

Lee Higbee

May 1990 **ACM SIGARCH Computer Architecture News**, Volume 18 Issue 2

Full text available:  pdf(764.51 KB) Additional Information: [full citation](#), [index terms](#)



**11 A simulation of operations of a quick-service steak house restaurant**

Haluk Bekiroglu, Turan Gonen

January 1977 **Proceedings of the 9th conference on Winter simulation - Volume 2**

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Operations of a steak-house restaurant in St. Ann, Missouri is simulated using GPSS(General Purpose Simulation System). Objective is to eliminate the long waiting lines. Two models are developed to simulate the actual situation and the proposed change to the restaurant. It is found that the change in facilities greatly improved the efficiency of the restaurant and increased profits by eliminating the problem of loss of customers over waiting time.



**12 LAMBDA: A quick, low cost layout design system for master-slice LSI s**

T. Matsuda, T. Fujita, K. Takamizawa, H. Mizumura, H. Nakamura, F. Kitajima, S. Goto

January 1982 **Proceedings of the 19th conference on Design automation**

Full text available:  pdf(735.42 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)



This paper describes an automatic/interactive layout design system for designing master-slice LSI chips, which places function blocks and gives wiring patterns on the chip. Since 100% routing is essential for master-slice layout design, it is urgently required to establish a strong CAD system, which significantly reduces the design time. The LAMBDA system has been developed to achieve complete net connectivity in as short a design time as possible, where efficient automatic procedures are i ...



**13 Automatic layout of low-cost quick-turnaround random-logic custom LSI devices**

A. Feller

June 1976 **Proceedings of the 13th conference on Design automation**

Full text available:  pdf(860.77 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper discusses the application of the RCA-developed standard cell approach for generating low-cost, quick-turnaround random-logic LSI arrays using automatic placement and routing computer programs. Originally developed with government support<sup>1</sup> for two-phase PMOS dynamic logic in 1967, the programs have been expanded to include many technologies<sup>2,3</sup> and have gone through extensive evolutionary and revolutionary changes during the past 10 years. ...



**14 PRONTO: Quick PLA product reduction**

Jorge F. Martinez-Carballedo, V. Michael Powers

June 1983 **Proceedings of the 20th conference on Design automation**

Full text available:  pdf(677.54 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)



A PLA is a regular, modular combinational function implementation popular in VLSI design. Among methods which reduce the cost of implementing a given PLA specification, reducing the number of product terms is particularly fruitful. Optimum product term reduction for practical-sized problems is prohibitively complex. Previous suboptimal, heuristic product term reducers have been iterative or indirect. PRONTO is a direct, one-pass method for heuristically reducing the number of pro ...

**15 Dynamic services and analysis: Make it fresh, make it quick: searching a network of personal webservers**

Mayank Bawa, Roberto J. Bayardo, Sridhar Rajagopalan, Eugene J. Shekita

May 2003 **Proceedings of the twelfth international conference on World Wide Web**

Full text available:  pdf(500.28 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Personal webservers have proven to be a popular means of sharing files and peer collaboration. Unfortunately, the transient availability and rapidly evolving content on such hosts render centralized, crawl-based search indices stale and incomplete. To address this problem, we propose YouSearch, a distributed search application for personal webservers operating within a shared context (e.g., a corporate intranet). With YouSearch, search results are always fast, fresh and complete -- properties we ...

**Keywords:** P2P, decentralized systems, information communities, intranet search, peer-to-peer networks, web search

**16 Quick simulation of rare events in networks**

R. D. Fresnedo

October 1989 **Proceedings of the 21st conference on Winter simulation**

Additional Information: [full citation](#), [citations](#), [index terms](#)



**17 Quick-fix consulting: a user disservice**

M. L. Mason

October 1989 **Proceedings of the 17th annual ACM SIGUCCS conference on User Services**

Full text available:  pdf(573.27 KB) Additional Information: [full citation](#), [abstract](#), [index terms](#)



In the face of expanding technology and demands for service accompanied by less-rapidly expanding budgets, consultants in the user services sections of university computing centers must be creative in their approach to distributing their consulting expertise. Quick-fix consulting is a user disservice. It must be replaced by consulting which begins with identifying and serving the individual user's immediate need and is followed by user education along with planning and preparation for the f ...

**18 Session 3C: Quick and good facility location**

Mikkel Thorup

January 2003 **Proceedings of the fourteenth annual ACM-SIAM symposium on Discrete algorithms**

Full text available:  pdf(765.85 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)



We consider the facility location problem with shortest path distances in a weighted graph. W.h.p., we get an approximation factor of 1.62 in  $O(n + m)$  time with  $n$  and  $m$  the number of nodes and edges. Also, as a kind of warm-up, for a metric with a constant-times distance oracle, we get the factor 1.62 deterministically in  $O(n^2 \log n)$  time. Our results build on a recent facility location algorithm of Jain, Mahdian, and Saberi (STOC ...

**Keywords:** efficient approximation algorithms, facility location, shortest paths

**19 Session P6: displays and color maps: The "Which Blair Project": a quick visual method for evaluating perceptual color maps** 

Bernice E. Rogowitz, Alan D. Kalvin

October 2001 **Proceedings of the conference on Visualization '01**

Full text available:  pdf(408.79 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

 Publisher Site

We have developed a fast, perceptual method for selecting color scales for data visualization that takes advantage of our sensitivity to luminance variations in human faces. To do so, we conducted experiments in which we mapped various color scales onto the intensitiy values of a digitized photograph of a face and asked observers to rate each image. We found a very strong correlation between the perceived naturalness of the images and the degree to which the underlying color scales increased mon ...

**Keywords:** human color vision, internet color, perceptual color scales, visual artifacts in visualization

**20 HPCA-8 work-in-progress session: A novel associative memory architecture for quick matching** 

Frank Wang, Na Helian, Farhi Marir

June 2002 **ACM SIGARCH Computer Architecture News**, Volume 30 Issue 3

Full text available:  pdf(170.39 KB) Additional Information: [full citation](#), [references](#), [index terms](#)

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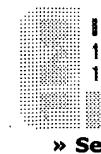
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